EXPERIMENT 3 DESIGN CHECKS

Syntactic Issues
1. Team information in the wrong place
2. A name is missing
3. The section name is missing
4. More than one schematic sheet used for design
5. The circuit is spread all across the sheet
6. Components are too close to each other
7. Blocks (subcircuits) too close to each other
8. Components of block too far from each other
9. Lines with unnecessary turns
10. Tangled wires
11. Wires over components
12. Dangling wires
13. Unconnected components
14. Input lines short circuited
15. Component outputs short circuited
16. Output lines short circuited
17. Component outputs short circuited to GND/VCC
18. Component outputs unused
19. Components/wires not labelled
20. Labels/attributes/components overlap

Design Issues
4. Number of gates used too high
5. Number of XDBs used too high
6. Unnecessary buffers
7. Unnecessary pads
8. The circuit does not work completely:
9. Other

- NO MISTAKES
CONTACTS:

1) Students can see the professor and teaching adjuncts (TAs), about the lectures, homework, and lab experiments.

2) Professor’s contact information:
   Room: 114 LC    (718) 260-3101    Fax: (718) 260-3609    haldun@photon.poly.edu
   Open-door policy to see the professor. If the door is closed, he might be in the lab.
   Present in the lab: Mondays (4-6), Wednesdays (3-5) and Fridays (2-4)

3) Below are the contact information and assignments of the TAs:
   Nikhil Joshi: 001LC, (718) 260-4011, njoshi01@utopia.poly.edu
   - Present in lab session: Tuesday: 3-5:50 (B)

   Sapan Shenoy: 102RH, (718) 260-3731, ssheno01@utopia.poly.edu
   - Present in lab sessions: Tuesday: 6-8:50 (C), Thursday: 6-8:50 (A)

   Jeff Tao: 233LC, (718) 260-3420, jefftao@photon.poly.edu
   - Present in lab session: Wednesday: 6-8:50 (D)

   Bo Yang: 001LC, (718) 260-4011, yangbo@photon.poly.edu
   - Present in lab session: Wednesday: 6-8:50 (D)
   - Grading the homework

   Peng Yao: 004LC, (718) 260-3975, yaopeng@utopia.poly.edu
   - Present in lab sessions: Tuesday: 3-5:50 (B), Thursday: 6-8:50 (A)

4) All handout and lab files are at the course web site: http://cis.poly.edu/cs2204

5) Students are asked to give feedback about the announced lab hours in case the hours are not sufficient and/or need to be rescheduled.

6) When short-term problems are encountered in PC labs, students are advised to contact: help@duke.poly.edu or (718) 260-3123 or go to Room: 337 RH.

For CS2204 lab related issues and to have the lab open, students need to contact the CIS lab supervisor Mr. Keni Yip at (718) 260-3023, keni@poly.edu. His office is 225RH

For longer-term problems in PC labs and any other matter, students should not hesitate to contact the professor and the TAs.